Fig.1

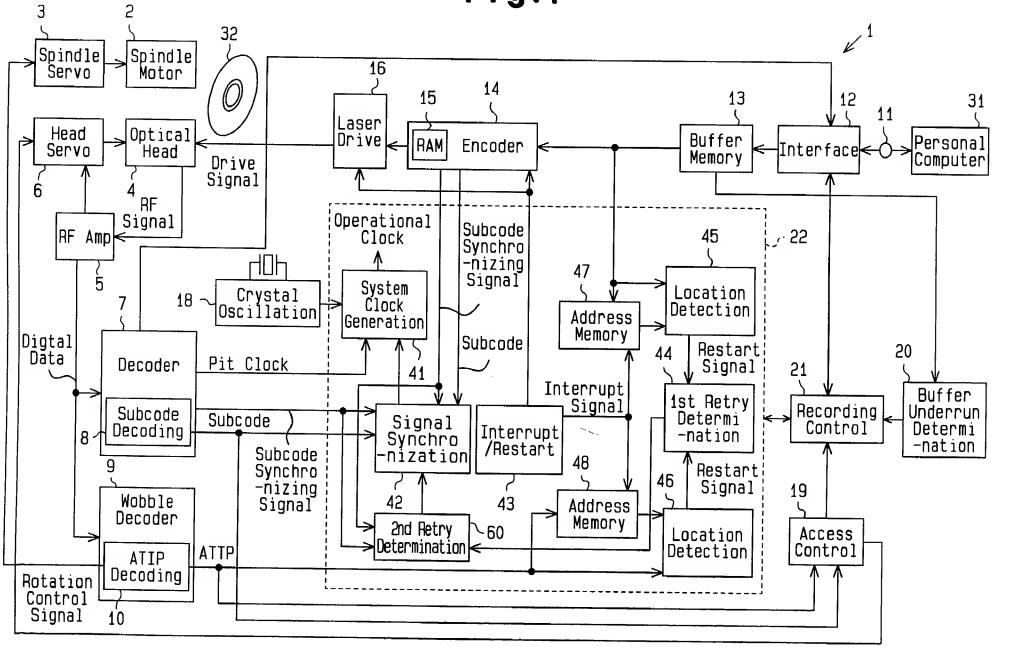

3
Spindle Servo
2
Spindle Motor
32
1
16
Laser Drive
15
RAM
14
Encoder
13
Buffer Memory
12
Interface
11
31
Personal Computer
Head Servo
Optical Head
Drive Signal
6
4
RF Signal
RF Amp
5
Operational Clock
Subcode Synchro -nizing Signal
22
45
47
Location Detection
Address Memory
18
Crystal Oscillation
System Clock Generation
7
Digtal Data
Decoder
Pit Clock
41
Subcode
Restart Signal
44
20
Interrupt Signal
21
Subcode Decoding
8
Subcode
Signal Synchro -nization
Interrupt /Restart
1st Retry Determi -nation
Recording Control
Buffer Underrun Determi -nation
9
Subcode Synchro -nizing Signal
48
Restart Signal
Wobble Decoder
42
43
46
19
2nd Retry Determination
60
Address Memory
Location Detection
Access Control
ATIP Decoding
ATTP
Rotation Control Signal
10

Fig.2(a)

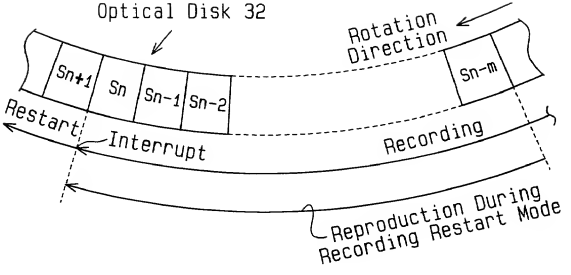

Optical Disk 32
Rotation Direction
Sn+1
Sn
Sn-1
Sn-2
Sn-m
Restart
Interrupt
Recording
Reproduction During Recording Restart Mode

Fig.2(b)

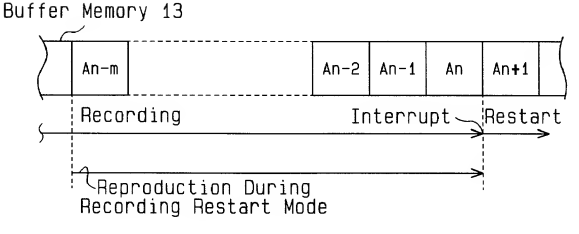

Buffer Memory 13
An-m
An-2
An-1
An
An+1
Recording
Interrupt
Restart
Reproduction During Recording Restart Mode

Fig.3

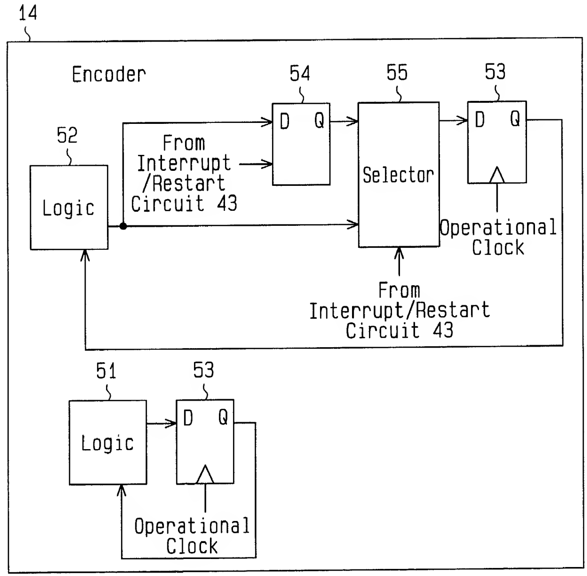
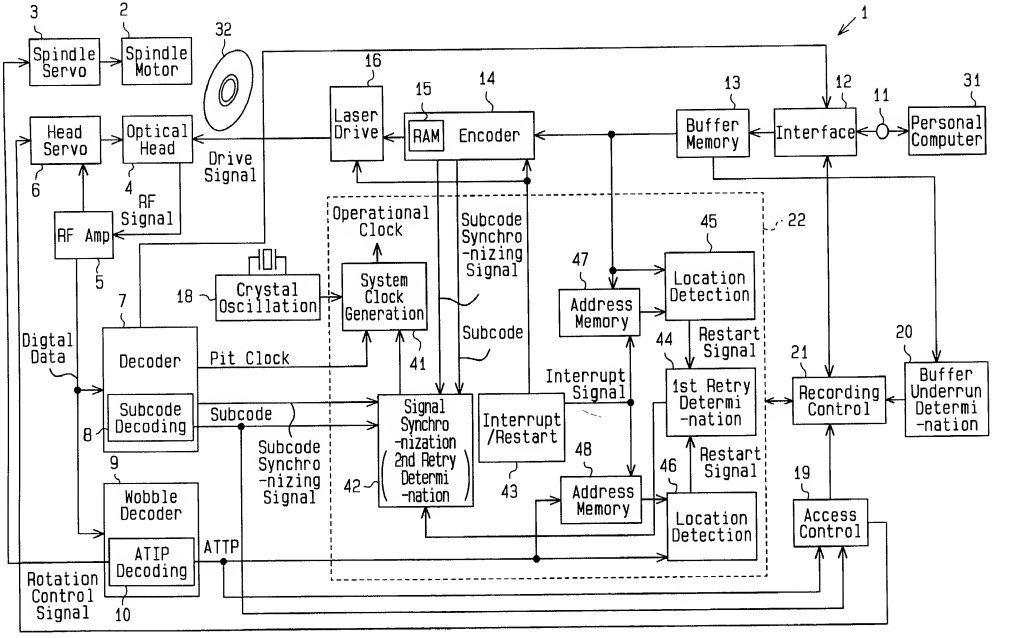

14
Encoder
52
Logic
54
D Q
55
Selector
53
D Q
From
Interrupt
/Restart
Circuit 43
Operational
Clock
From
Interrupt/Restart
Circuit 43
51
Logic
53
D Q
Operational
Clock

Fig.4


1
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Drive Signal
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Subcode Decoding
8
Subcode
Signal Synchro -nization 2nd Retry Determi -nation
42
Interrupt /Restart
43
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Address Memory
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Location Detection
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ATIP Decoding
ATTP
Rotation Control Signal
10